



Strategic Electrical Schematic Management - Looking to the future, now.

Introduction

Electrical schematic error checking is an important step in the design of electrical systems to ensure their functionality and safety.

There are several methods and techniques that can be employed for electrical schematic error checking, most of which are manual. In this paper we discuss current methods and techniques available.

Commonly used methods to check electrical schematics

Manual Inspection: Perform a thorough manual inspection of the schematic diagram, while paying attention to the components chosen, net naming, and board architecture. Making sure the design is efficient and incorporates all the wanted functionality. Cross-reference the schematic with other related documents such as datasheets, bill of materials, and design specifications. Verify that the component values, ratings, and part numbers are consistent across all documents.

Peer Review: Seek input from other knowledgeable individuals, such as colleagues or experts in the field, to review the schematic and provide feedback. Fresh perspectives can help identify errors or suggest improvements.

Simulation and Analysis Tools: Utilize electrical simulation software or analysis tools to simulate the behavior of the circuit and validate its performance. These tools can help identify potential errors or issues in the schematic design.

Error Trapping Techniques: Use error trapping techniques, such as error-checking algorithms or design rule checks (DRC), provided by schematic capture software. These tools can automatically flag potential errors, such as unconnected pins, duplicate net names, or inconsistent component values.





It's important to note that the combination of these methods can provide a comprehensive approach to electrical schematic error checking, minimizing the likelihood of errors and ensuring a reliable and safe electrical system.

But these processes are all cumbersome and take up a lot of the engineers' time – so several methods are out there today to try ease this process.

The Next Generation of Electrical Schematic Inspection OR Automating Electrical Schematic Inspection

CADY - AI Powered Automated Electrical Schematic Inspection

CADY implements AI technology to perform automatic inspection and verification of electrical schematics - enabling engineers to improve and expedite the design process, save money & resources, reduce time to market and contribute to the quality, reliability, and safety of the final product.

CADY's system realises requirements and properties of the components in the schematic from their corresponding datasheets and checks them against the schematic wiring connections to detect errors.

The system issues a report detailing errors such as: missing pull up/down resistors, voltage range violations, communication lines mismatch, potential capacitor breach and many more.

Many of CADY's features can save a lot of time for the engineer, as it able to detect errors that are hard to find, such as leakage between power to ground, wrong value of a passive component, not enough capacitance on power lines and many more!

In addition, CADY's system offers configurable best-practice recommendations, such as missing test points on important nets, missing capacitors on reset nets, and many others.

These recommendations are based on datasheets parsed information and common knowledge collected from its users, and each new user can choose which recommendation they would like to enforce on their design.

Within a few minutes to hours after the user uploads their Netlist and BOM, CADY's system generates a comprehensive report that includes all errors, warnings and best-practice recommendations the designer can use to avoid the next re-spin.

